

HR618802

Electrical Specification @25°C

RoHS Compliant (PB Free)

Operating Temperature Range: 0°C ~ +70°C

Isolation: 1500Vrms 0.5mA 60sec (Primary to Secondary)

OCL: 350uH Minimum @100KHz 100mV with 8mAADC

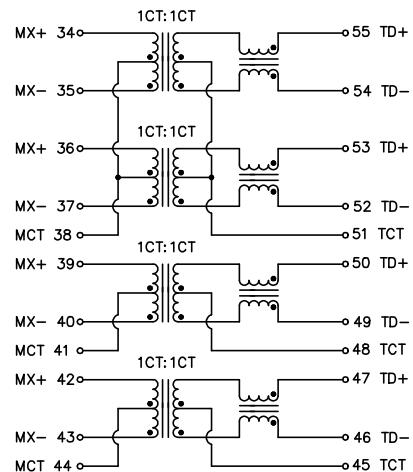
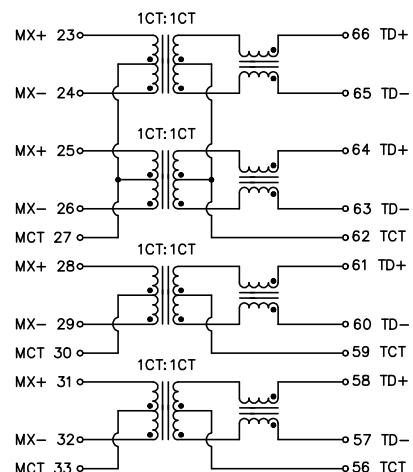
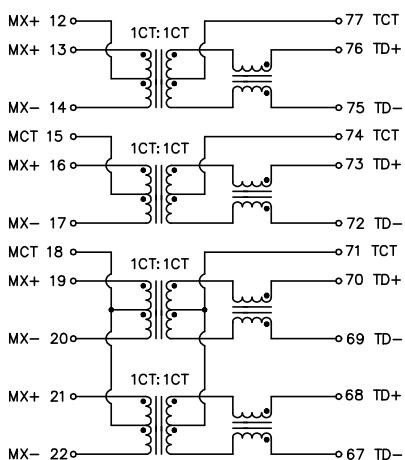
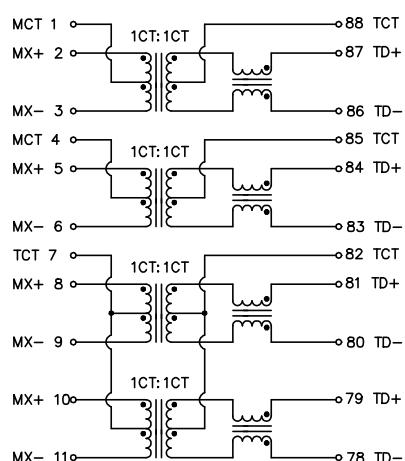
Insertion Loss: -1.0dB Maximum @1MHz ~ 100MHz

Return Loss: -18dB Minimum @1MHz ~ 30MHz / -13.5dB Minimum @30MHz ~ 60MHz
-12dB Minimum @60MHz ~ 80MHz / -10dB Minimum @80MHz ~ 100MHz

Common Mode Rejection: -30dB Minimum @1MHz ~ 100MHz

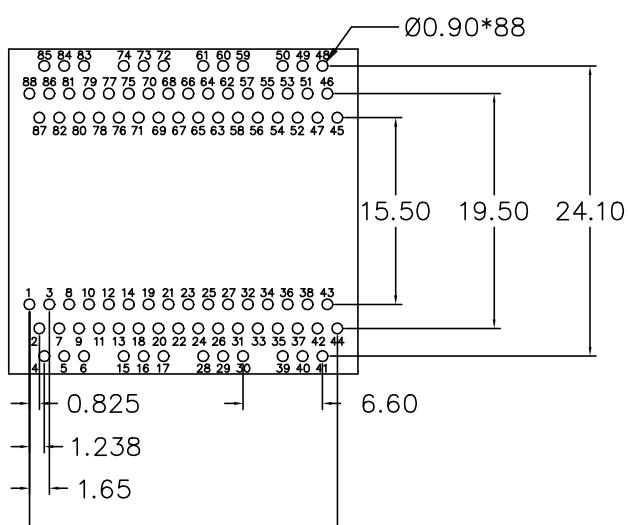
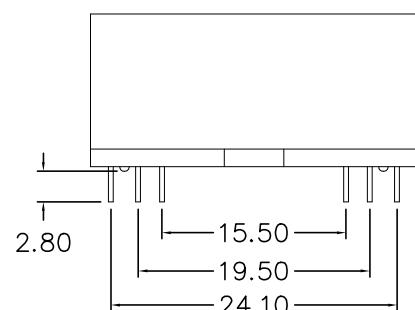
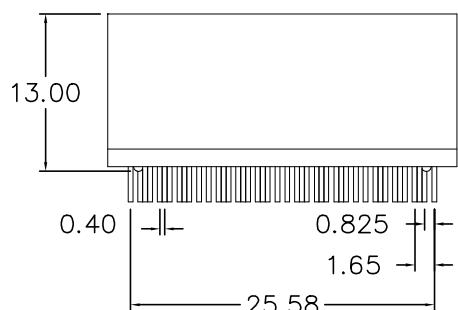
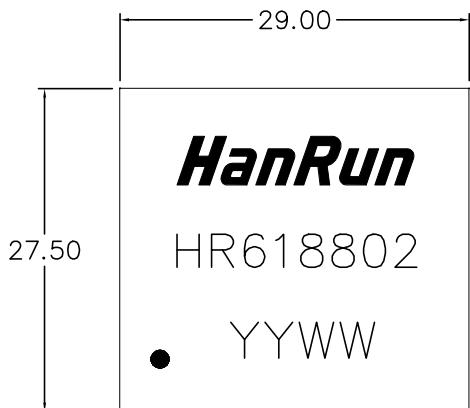
Crosstalk: -30dB Minimum @1MHz ~ 100MHz

Schematics



HR618802

Mechanical Dimensions



SUGGESTED PCB PAD LAYOUT

Dimensions in mm

Unless otherwise specified, Tol.: .xx ±0.25

REV.: 00

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Specifications subject to change without notice. Refer to the approval sheet for lastly edition.