

HR644840E

Electrical Specification @25°C

RoHS Compliant (PB Free)

Operating Temperature Range: -40°C ~ +85°C

Isolation: 1500Vrms 0.5mA 60sec (Primary to Secondary)

Turns Ratio: 1CT:1CT ±3% (Primary : Secondary)

Chip Side OCL: 120uH Minimum @100KHz 100mV with 19mADC

350uH Minimum @100KHz 100mV with 8mADC

Insertion Loss: -1.2dB Maximum @100KHz / -1.5dB Maximum @1MHz ~ 100MHz

-2.0dB Maximum @100MHz ~ 125MHz

Return Loss: -16dB Minimum @1MHz ~ 40MHz / -12.5dB Minimum @40MHz ~ 60MHz

-10dB Minimum @60MHz ~ 80MHz / -8dB Minimum @80MHz ~ 100MHz

Common Mode Rejection Ratio: -50dB Minimum @2MHz

-21dB Minimum @30MHz ~ 100MHz / -15dB Minimum @100MHz ~ 200MHz

Different to Common Mode Rejection: -50dB Minimum @2MHz

-21dB Minimum @30MHz ~ 100MHz / -15dB Minimum @100MHz ~ 200MHz

Crosstalk: -50dB Minimum @1MHz / -28dB Minimum @10MHz ~ 100MHz

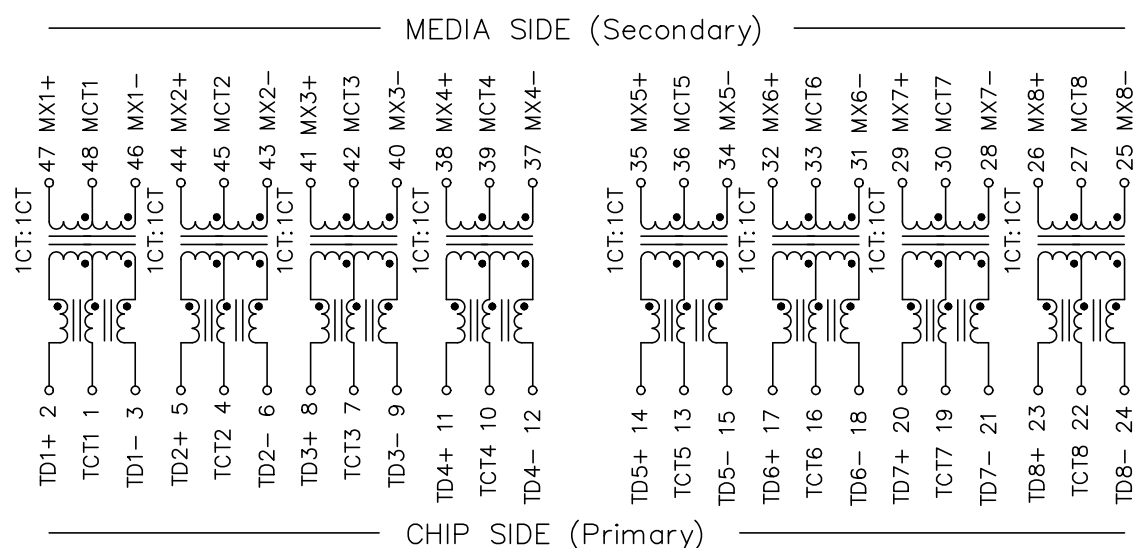
DC Resistance 1/2 Winding: 0.65 Ohms Maximum

DC Resistance Imbalance: ±0.065 Ohms (Center Tap Symmetry)

Balance DC Line Current 600mAdc Maximum WHEN USING 2 CHANNELS

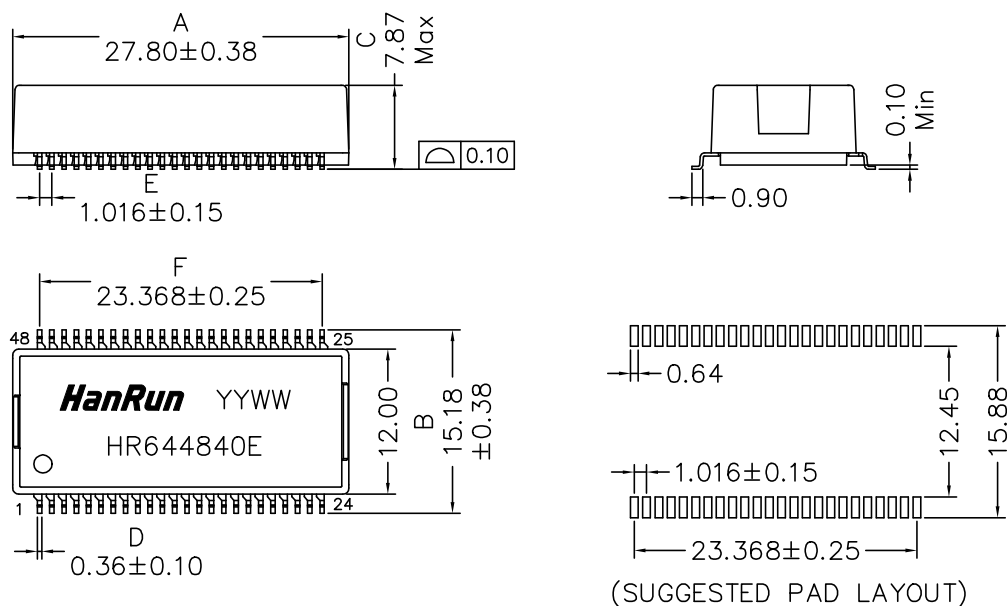
Balance DC Line Current 1.2Adc Maximum WHEN USING 4 CHANNELS

Schematics



HR644840E

Mechanical Dimensions



Dimensions in mm

Unless otherwise specified, Tolerance: .xx ±0.25

REV.: 00

2 of 2